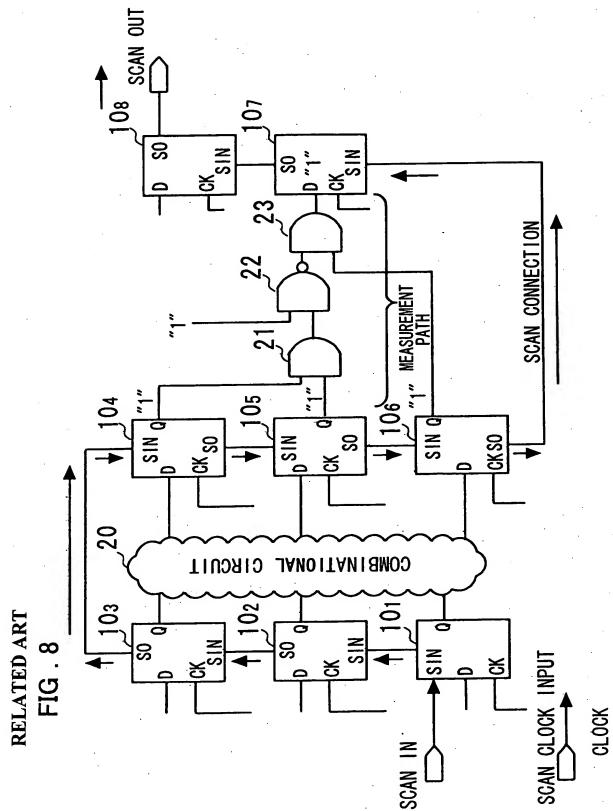


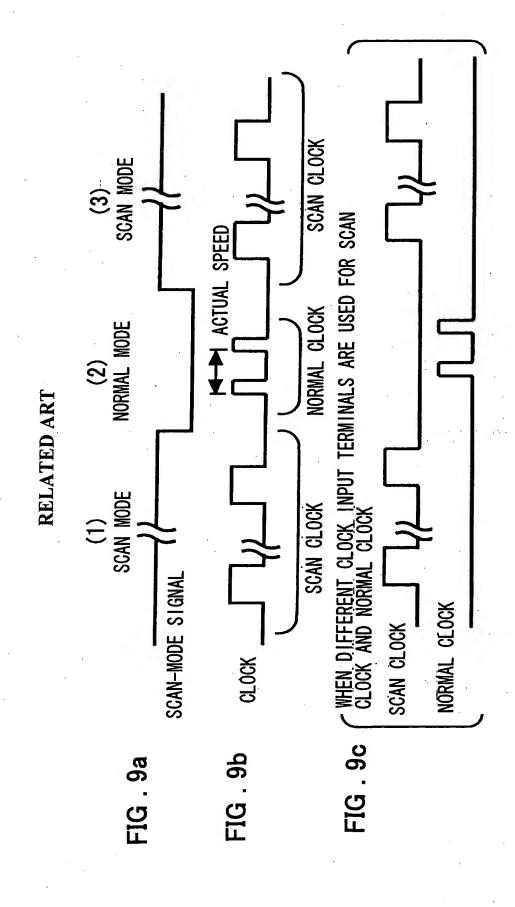


SCAN OUT 8 MEASUREMENT PATH 105 SING ONISO S SIN S 交 쑹 20 NORMAL CLOCK INPUT (2 CLOCKS) COMB I NATI ONAL CIRCUIT 000 0 RELATED ART SIN -SIN S SIN 용 FIG. 7 CL00K SCAN IN











## RELATED ART

